



Docket No.: 8733.345.10-US

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Woo-Hyuk Choi

Application No.: 10/774,517

Filed: February 10, 2004

For: A METHOD OF MANUFACTURING AN

ARRAY SUBSTRATE FOR USE IN A LCD

DEVICE

Confirmation No.: 8692

Art Unit: 2871

Examiner: Dung T. Nguyen

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the aboveidentified patent application. The official Filing Receipt received by Applicants, a copy of which is attached hereto, has errors in the following:

Domestic Priority Data as Claimed by Applicant:

This application is a DIV of 09/745,526 12/26/2000 PAT 6,331,949

Should be

This application is a DIV of 09/745,527 12/26/2000 PAT 6,717,631

Title:

Method of manufacturing an array substrate for use in a LCD device

Docket No.: 8733.345.10-US

Should be

A method of manufacturing an array substrate for use in a LCD device

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: August 2, 2004

Respectfully submitted,

Reg. No.

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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office
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APPL NO.

Song K. Jung

FILING OR 371 (c) DATE

MCKENNA LONG & ADRIDGE LLP

ART UNIT

FIL FEE REC'D 770

ATTY.DOCKET NO

DRAWINGS

TOT CLMS

IND CLMS

10/774.517 02/10/2004

1900 K Street, N.W.

Washington, DC 20006

2871

8733.345.10-US

CONFIRMATION NO. 8692

OC000000012545209*

Date Mailed: 05/06/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Woo-Hyuk Choi, Kumi-shi, KOREA, REPUBLIC OF;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/745,526 12/26/2000 PAT 6,331,949

Should be

Should 6,717,631

Foreign Applications

REPUBLIC OF KOREA 1999-62983 12/27/1999 ---

If Required, Foreign Filing License Granted: 05/05/2004

Projected Publication Date: 08/12/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing an array substrate for use in a LCD device

Preliminary Class

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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